QI LING

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RESEARCH INTERESTS

I want to make computer systems more efficient, reliable, and secure. Currently, my project focuses on scalable isolation techniques, specifically identifying and addressing bottlenecks in isolation techniques to enhance performance and scalability. Still, my interest spans many other fields in computer architecture and operating systems.

EDUCATION

Purdue University
 Doctoral Degree in CS
 Advisors: Prof. Kazem Taram and Prof. Pedro Fonseca
 University of Michigan
 Bachelor's Degree in CS
 Advisors: Prof. Baris kasikci and Prof. Shuwen Deng
 Shanghai Jiao Tong University
 Bachelor's Degree in ECE
 Aug. 2024 – Present
 Overall GPA: NA
 Overall GPA: NA
 Overall GPA: NA
 Overall GPA: 3.92/4
 Overall GPA: 3.92/4
 Overall GPA: 3.74/4

AWARDS AND HONORS

Advisor: Prof. Yibo Pi

• ACM Student Research Competition 2nd Place Award Presented a poster and gave a 10min talk at MICRO'23	2023
• ACM MICRO Student Travel Grant	2023
• Dean's Honor List	2022,2023
• Jackson and Murial Lum Scholarship 5 recipients each year	2022, 2023
• SJTU Undergraduate Excellence Scholarship	2021

PEER-REVIEWED WORKS

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•	Accurate Detection and Assessment of Spectre-PHT Gadget Qi Ling, Yujun Liang, Yi Ren, Baris Kasikci, Shuwen Deng Won 2nd Place at MICRO'23 Student Research Competition Full paper in submission	July 2024
•	Towards Fine-Grained, High-Coverage Internet Monitoring at Scale Hongyu Wu, Qi Ling, Penghui Mi, Chaoyang Ji, Yinliang Hu, Yibo Pi The 7th Asia-Pacific Workshop on Networking (APNet'23)	June 2023

RESEARCH EXPERIENCE

• SecArch & RSSys, Purdue University
Advisors: Kazem Taram and Pedro Fonseca

Evaluating scalability bottlenecks for different isolation techniques.

Explored scalability issues in virtualization environments, focusing on inter-VM/container scalability.

 Conducted case study on Redis clusters in Kubernetes to observe scalability bottlenecks and performance degradation.

• EFESLAB, University of Michigan Advisors: Baris Kasikci and Shuwen Deng

Dec. 2022 - Dec. 2023

Detecting and evaluating Spectre-PHT gadgets in programs.

- Identified the **Timing Condition** of Spectre-PHT gadget.
- Presented a systematic study and exploration of windowing power.
- Proposed and implemented a new approach, which describes the timing condition with a DAG graph, models the windowing power with graph algorithms, and quantifies the gadget's exploitability with runtime measurement.
- Validated our approach and evaluated it against SOTA scanners on 2 micro-benchmarks, 6 userspace programs, and the Linux kernel. Identify 503 gadgets reported by SOTA scanners as unexploitable.

Network Measurement and System Lab, SJTU Advisor: Yibo Pi

Aug. 2021 - Sep. 2022

Optimizing the accuracy and coverage of large-scale network monitoring.

- Challenged two root assumptions of conventional network monitoring practice with experiments.
- Proposed and implemented a greedy end-to-end network monitoring approach.
- Evaluated the accuracy, coverage and overhead of our approach.

TEACHING EXPERIENCE

• Teaching assistant, Purdue University CS250 Computer Architecture Aug. 2024 - Present

TECHNICAL SKILLS

- Programming Languages: Assembly, Verilog, C/C++, Rust
- Software Tools: Kubernetes, LLVM, Honggfuzz, Syzkaller, Gem5